

Amendments to the Specification:

Please delete the paragraph beginning on page 8, line 11, and replace with the following:

A PCI host bridge **130** provides an interface for a PCI bus **131** to connect to I/O bus **112**. PCI I/O adapter **136** is connected to I/O slot **176**, which is connected to PCI-to-PCI bridge **132** by PCI bus **133**. PCI-to-PCI bridge **132** is connected to PCI bus **131**. This PCI bus also connects PCI host bridge **130** to the service processor mailbox interface and Industry Standard Architecture (ISA) [[ISA]] bus access pass-through logic **194** and PCI-to-PCI bridge **132**. Service processor mailbox interface and ISA bus access pass-through logic **194** forwards PCI accesses destined to the PCI/ISA bridge **193**. Non-Volatile Random Access Memory (NVRAM) [[NVRAM]] storage **192** is connected to the ISA bus **196**. Service processor **135** is coupled to service processor mailbox interface and ISA bus access pass-through logic **194** through its local PCI bus **195**. Service processor **135** is also connected to processors **101-104** via a plurality of Joint Test Action Group (JTAG) [[JTAG]]/ I^2C busses **134**. JTAG/ I^2C busses **134** are a combination of JTAG/scan busses (see IEEE 1149.1) and Phillips I^2C busses. However, alternatively, JTAG/ I^2C busses **134** may be replaced by only Phillips I^2C busses or only JTAG/scan busses. All SP-ATTN signals of the host processors **101**, **102**, **103**, and **104** are connected together to an interrupt input signal of the service processor. The service processor **135** has its own local memory **191**, and has access to the hardware OP-panel **190**.

Please delete the paragraph beginning on page 12, line 28, and replace with the following:

Operations of the different partitions may be controlled through a hardware management console, such as console **280** [[264]]. Console **280** [[264]] is a separate data processing system from which a system administrator may perform various functions including reallocation of resources to different partitions.